ABSTRACT OF THE DISCLOSURE

In a video decoding device for decoding a coded video signal which is supplied as packets each of which containing a plurality of coded block data units which have been generated by encoding blocks each of which is composed of a predetermined number of pixels of a frame of a video signal, an error detection section conducts error detection for each packet by use of error detection code which has been contained in the packet. A packet partitioning section partitions the packet into the coded block data units, while outputting an address signal indicating addresses of blocks that have been contained in the packets to which the errors have occurred. A video decoding section successively decodes the coded block data units. A first invalid block judgment section conducts an invalid block judgment process only for blocks that are designated by the address signal. A first invalid block concealment section conducts a concealment process (pixel value correction) for blocks that have been judged by the first invalid block judgment section as invalid blocks. By such composition and operation of the video decoding device, the load on the first invalid block judgment section can be reduced much, thereby the invalid block judgment process and the concealment process can be executed efficiently and effectively.